

AMENDMENT TO THE SPECIFICATION

On page 2, line 23, please insert the following new paragraph:

FIG. 7 is a block diagram of the operation of the logic unit used in accordance with an embodiment of the present invention.

On page 4, line 34, please delete the equation and replace with the following:

$$\mu_i = \sum_{j=1}^n s_{ij} r_{ij}$$